IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor : Minari ARAI

Filed : Concurrently herewith

For : SEMICONDUCTOR INTEGRATED....

Serial No. : Concurrently herewith

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

December 5, 2003

INFORMATION DISCLOSURE STATEMENT

SIR:

In order to comply with discretionary regulations 37 CFR 1.97 and 1.98, attached hereto is a copy of Form PTO-1449 and copies of the documents listed thereon. These documents contain information which the Examiner may consider to be important in deciding whether to issue a patent in the instant application.

For documents written in a language other than English, English language abstracts are annexed thereto explaining relevancy in accordance with 37 CFR §1.98 (a)(3).

As this Statement is being filed prior to issuance of a first Office Action, no fee is due.

The present Disclosure Statement is being submitted in compliance with 37 CFR 1.56 as an Examiner might consider any cited document important in deciding whether to allow the application to issue as a patent, but the citation of each document is not to be construed as an admission that such document is necessarily relevant or prior art. No representation is intended

that the cited documents represent the results of a complete search, and it is anticipated that the Examiner in the normal course of examination, will make an independent search and will determine the best prior art consistent with 37 CFR 1.104 (a), and in the course of such search

Early and favorable consideration is respectfully solicited.

Any fee due with this paper may be charged on Deposit Account 50-1290.

will review for relevance every document cited on the attached form even if not initialed.

Respectfully submitted,

Michael I. Markowitz Reg. No. 30,659

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Docket No: NECE 20.780

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		2000-163999		JP	Fujitsu Ltd.		06-16-2000					
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